

EDQD400-x

400Gbps OSFP to QSFPDD Passive High Speed Cable

PRODUCT FEATURES

- Products Compliance with CMIS4.0, OSFP_MSA, QSFP DD MSA
- Ethernet-Compliance with IEEE802.3cd
- Support 56G (PAM4) electrical data rates/channel
- Support I2C two - line string interface, easy to control
- Support for hot plugging
- Low crosstalk/Low power
- Maximum Link Length: up to 3m
- ROHS Compliance

APPLICATIONS

- 400G Ethernet
- SWITCH/ Router
- Data storage and communication industry
- Data center, cloud server

Description

The 400G OSFP56 Passive Direct Attach Copper Twinax Cable is designed for use in 400GBASE Ethernet., OSFP56 is the module and cage/connector system based on current OSFP, targeting to support the 56Gb/s per lane speed in a 8x lane OSFP system and to enable the OSFP 400G interconnect ecosystem. This will greatly help the legacy OSFP users upgrade the link bandwidth to 400G per port with lower cost and shorter transition time.

QSFP-DD (quad small form-factor pluggable double density) doubles the density of QSFP interconnects with an eight-lane electrical interface capable of 28Gbps NRZ or 56Gbps PAM-4 to achieve 200 or 400Gbps aggregate per port. The QSFP-DD portfolio's backwards compatibility allows existing QSFP modules to be plugged into QSFP-DD ports, provide low loss, less skew and better NEXT. providing superior thermal and signal integrity performance.

Information

Part No.	Description
EDQD400-x	400Gbps OSFP To QSFPDD Passive High Speed Cable xM

PN	Data Rate	Length	Wire Gauge	Temp.Range
EDQD400-0.5-30	400G	0.5M	30AWG	0-70°C
EDQD400-1-30	400G	1M	30AWG	0-70°C
EDQD400-1.5-30	400G	1.5M	30AWG	0-70°C
EDQD400-2-28	400G	2M	28AWG	0-70°C
EDQD400-3-27	400G	3M	27AWG	0-70°C

Wiring Diagram

P1			P2
GND	1	---	23
TX2+	2	---	22
TX2-	3	---	21
GND	4	---	20
TX4+	5	---	25
TX4-	6	---	24
GND	7	---	61
TX6+	8	---	60
TX6-	9	---	59
GND	10	---	58
TX8+	11	---	61
TX8-	12	---	62
GND	13	---	63
GND	18	---	73
RX7-	19	<---	72
RX7+	20	<---	71
GND	21	---	70
RX5-	22	<---	75
RX5+	23	<---	74
GND	24	---	73
RX3-	25	<---	34
RX3+	26	<---	33
GND	27	---	32
RX1-	28	<---	37
RX1+	29	<---	36
GND	30	---	35
GND			GND

P1			P2
GND	31	---	4
RX2+	32	<---	3
RX2-	33	<---	2
GND	34	---	1
RX4+	35	<---	6
RX4-	36	<---	5
GND	37	---	4
RX6+	38	<---	41
RX6-	39	<---	40
GND	40	---	39
RX8+	41	<---	44
RX8-	42	<---	43
GND	43	---	42
GND	48	---	54
TX7-	49	---	53
TX7+	50	---	52
GND	51	---	51
TX5-	52	---	56
TX5+	53	---	55
GND	54	---	54
TX3-	55	---	15
TX3+	56	---	14
GND	57	---	13
TX1-	58	---	18
TX1+	59	---	17
GND	60	---	16
GND			GND

PINOUT

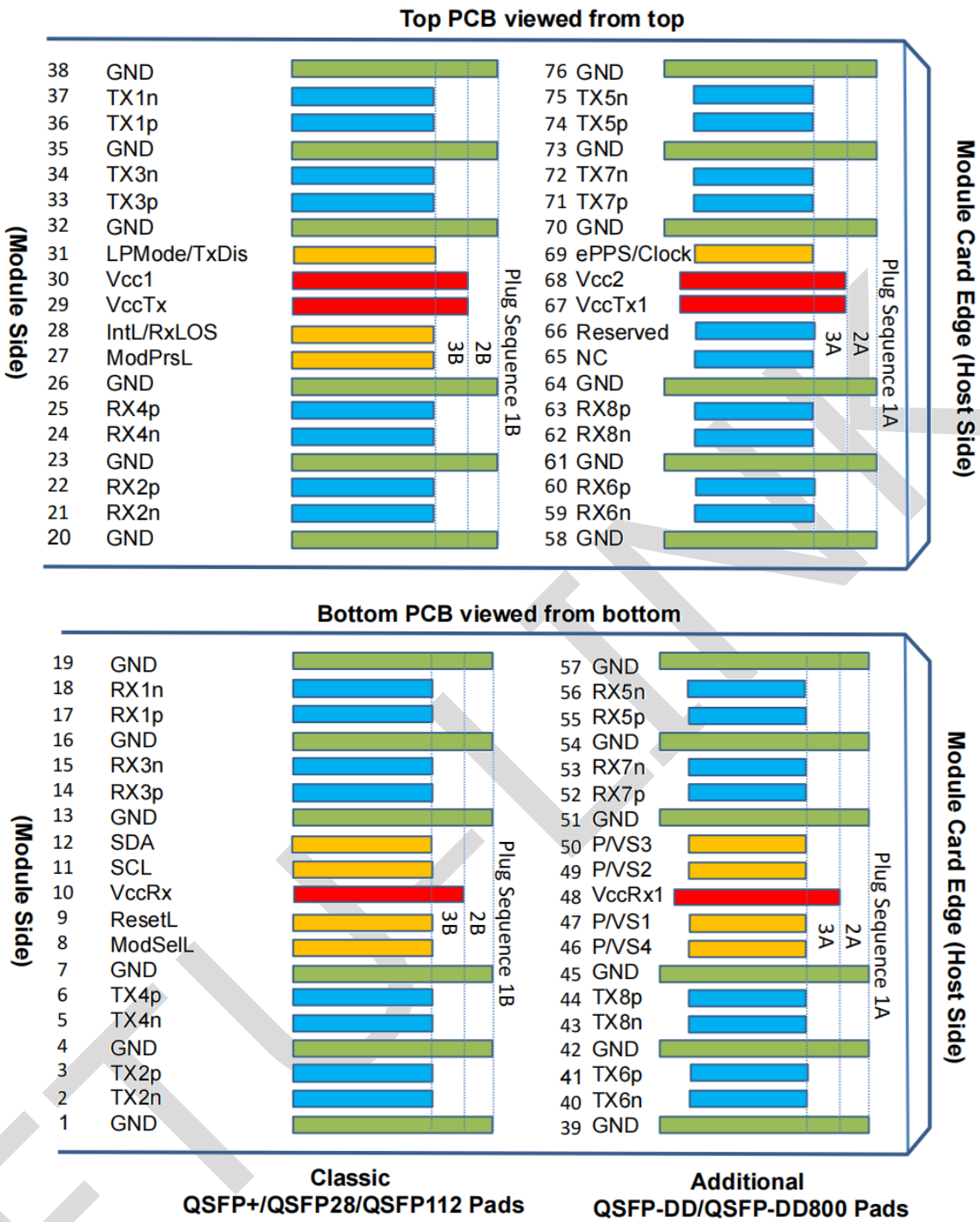
Top Side (viewed from top)

60	GND
59	TX1p
58	TX1n
57	GND
56	TX3p
55	TX3n
54	GND
53	TX5p
52	TX5n
51	GND
50	TX7p
49	TX7n
48	GND
47	SDA
46	VCC
45	VCC
44	INT/RSTn
43	GND
42	RX8n
41	RX8p
40	GND
39	RX6n
38	RX6p
37	GND
36	RX4n
35	RX4p
34	GND
33	RX2n
32	RX2p
31	GND

Bottom Side (viewed from bottom)

GND	1
TX2p	2
TX2n	3
GND	4
TX4p	5
TX4n	6
GND	7
TX6p	8
TX6n	9
GND	10
TX8p	11
TX8n	12
GND	13
SCL	14
VCC	15
VCC	16
LPWRT/PRSn	17
GND	18
RX7n	19
RX7p	20
GND	21
RX5n	22
RX5p	23
GND	24
RX3n	25
RX3p	26
GND	27
RX1n	28
RX1p	29
GND	30

----- Module Card Edge -----



Electrical Performance

Signal Integrity

ITEM		REQUIREMENT	TEST CONDITION
Differential Impedance	Cable Impedance	100±5Ω	Rise time of 25ps (20 % - 80 %).
	Paddle Card Impedance	100±10Ω	
	Cable	100+10/-15Ω	

	Termination Impedance		
Differential (Input/Output)Return loss S_{DD11}/S_{DD22}		$Return_loss(f) \geq$ Where f is the frequency in GHz $Return_loss(f)$ is the return loss at frequency f	$10MHz \leq f \leq 26.5GHz$
Differential to common-mode (Input/Output)Return loss S_{CD11}/S_{CD22}		$Return_loss(f) \geq$ Where f is the frequency in GHz $Return_loss(f)$ is the Differential to common-mode return loss at frequency f	$50MHz \leq f \leq 26.5GHz$
Common-mode to Common-mode (Input/Output)Return loss S_{CC11}/S_{CC22}		$Return_loss(f) \geq 2dB$ $0.05 \leq f \leq 19$ Where f is the frequency in GHz $Return_loss(f)$ is the common-mode to common-mode return loss at frequency f	$50MHz \leq f \leq 26.5GHz$
Differential Insertion Loss (S_{DD21} Max.)		(Differential Insertion Loss Max. For TPa to TPb Excluding Test fixture)	$50MHz \leq f \leq 26.5GHz$
		$Insertion_loss(f) \geq -17.16dB$ $0.05 \leq f \leq 13.28GHz$ Where f is the frequency in GHz $Insertion_Loss(f)$ Differential Insertion Loss at frequency f	
Insertion Loss Deviation		$-0.176*f - 0.7 \leq ILD \leq 0.176*f + 0.7$	$50MHz \leq f \leq 26.56GHz$
Differential to common-mode Conversion Loss-Differential Insertion Loss($S_{CD21}-S_{DD21}$)		$Conversion_loss(f) - IL(f) \geq$ Where f is the frequency in GHz $Conversion_loss(f)$ is the cable assembly differential to common-mode conversion loss $IL(f)$ is the cable assembly insertion loss	$50MHz \leq f \leq 26.5GHz$
[MDNEXT(multiple disturber near-end crosstalk)]		$\geq 35dB @26.5GHz$	$10MHz \leq f \leq 26.5GHz$
Intra Skew		10ps/m,	$10MHz \leq f \leq 19GHz$

Other Electrical Performance

ITEM	REQUIREMENT	TEST CONDITON
Low Level Contact Resistance	70milliohms Max. From initial.	EIA-364-23:Apply a maximum voltage of 20mV And a current of 100 mA.
Insulation Resistance	10Mohm(Min.)	EIA364-21:AC 300V 1minute
Dielectric Withstanding Voltage	NO disruptive discharge.	EIA-364-20:Apply a voltage of 300 VDC for 1minute between adjacent terminals And between adjacent terminals and ground.

Environment Performance

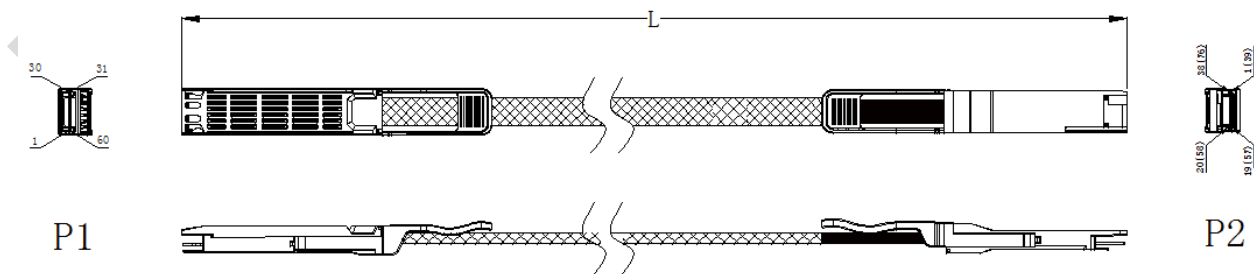
ITEM	REQUIREMENT	TEST CONDITON
Operating Temp. Range	0°C to +70°C	Cable operating temperature range.
Storage Temp. Range (in packed condition)	-40°C to +80°C	Cable storage temperature range in packed condition.
Thermal Cycling Non-Powered	No evidence of physical damage	EIA-364-32D, Method A, -25 to 90C, 100 cycles, 15 min. dwells
Salt Spraying	48 hours salt spraying after shell corrosive area less than 5%.	EIA-364-26
Mixed Flowing Gas	Pass electrical tests per 3.1 after stressing. (For connector only)	EIA-364-35 Class II,14 days.
Temp. Life	No evidence of physical damage	EIA-364-17C w/ RH, Damp heat 90°C at 85% RH for 500 hours then return to ambient
Cable Cold Bend	4H, No evidence of physical damage	Condition: -20°C±2°C, mandrel diameter is 6 times the cable diameter.

Mechanical and Physical Characteristics

ITEM	REQUIREMENT	TEST CONDITON
Vibration	Pass electrical tests per 3.1 after stressing.	Clamp & vibrate per EIA-364-28E, TC-VII, test condition letter – D, 15 minutes in X, Y & Z axis.
Cable Flex	No evidence of physical damage	Flex cable 180° for 20 cycles (±90° from nominal position) at 12 cycles per minute with a 1.0kg load applied to the cable jacket. Flex in the boot area 90° in each direction from vertical. Per EIA-364-41C
Cable Plug Retention in Cage	125 N Min. (OSFP) No evidence of physical damage 90N Min. (QSFP DD)	No functional damage to module, connector, or cage with latching mechanism activated. Per OSFP _Specification_ Rev5_0 5.0 Pull on cable jacket approximately 1 ft behind

	No evidence of physical damage	cable plug. No functional damage to cable plug below 90N. Per QSFP-DD Hardware Rev 5.1
Cable Retention in Plug	90N Min. No evidence of physical damage	Cable plug is fixtured with the bulk cable hanging vertically. A 90N axial load is applied (gradually) to the cable jacket and held for 1 minute. Per EIA-364-38B
Mechanical Shock	Pass electrical tests Per 3.1 after stressing.	Clamp and shock, 3 times in 6 directions, 100g, 6ms. per EIA-364-27B, TC-G
Cable Plug Insertion	OSFPmodule :40N Max.(55N) QSFP-DDmodule:90N Max	Per OSFP_Specification_Rev5_0 5.0 Module to be inserted into connector and cage with latch mechanism engaged. (55N if the cage has riding heatsink) Per QSFP-DD Hardware Rev 5.1
Cable plug Extraction	OSFPmodule :30N Max. (45N) QSFP-DDmodule:50N Max.	Module to be removed from connector and cage with latching mechanism disengaged. (45N if the cage has riding heatsink) Per OSFP_Specification_Rev5_0 5.0 Place axial load on de-latch to de-latch plug, Measure without the aid of any cage kick-out springs. Place axial load on de-latch to de-latch plug. Per QSFP-DD Hardware Rev 5.1
Durability	Module:50 cycles, No evidence of physical damage	Number of cycles for an individual module, to be tested with cage, connector, and module; latches may be locked out during testing Per OSFP_Specification_Rev5_0 5.0, perform plug & unplug cycles: Plug and receptacle mate rate: 250times/hour. 50times for QSFP-DD module (CONNECTOR TO PCB) Per EIA-364-09,

Outline drawing



Revision History

Version No.	Date	Description
1.0	Apr 25, 2022	Preliminary datasheet
2.0	September 28,2024	Product upgrades

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